

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Alexander Berger; Frank E. Kretz

Assignee: Tru-Si Technologies, Inc.

Title: Alignment of Semiconductor Wafers and Other Articles

Application No.: 09/905,218 Filing Date:

Filing Date: July 13, 2001

Examiner: Janice Lee Krizek

nice Lee Krizek Group Art Unit: 3652

Docket No.: M-11882 US

RECEIVED

San Jose, California June 25, 2003

Commissioner For Patents P.O. Box 1450

Alexandria, VA 22313-1450

**GROUP 3600** 

JUL 0 9 2003

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(B) WITH CERTIFICATION UNDER 37 C.F.R. § 1.97(E)(1)

Dear Sir:

Pursuant to 37 C.F.R. § § 1.56, 1.97 and 1.98, Applicants wish to call the documents listed on the enclosed Form PTO-1449 (1 page) to the Examiner's attention for the above-identified patent application. Copies of the listed documents are enclosed. The documents were first cited by the European Patent Office in a corresponding PCT Application

No. PCT/US 02/22375. The relevance of any foreign language documents is explained in the accompanying English abstracts and the enclosed PCT International Search Report and the relevance of Japanese document AN is also explained in the accompanying copy of Patent

Abstracts of Japanese more application of Patent Office.

07/18/2003 AGRAY1 01 FC:1806

10.00 DA

Citation of the above documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
  - 2. a representation that a search has been made, other than as described above; or

-1- Application No.: 09/905,218

3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

The undersigned hereby certifies in accordance with § 1.97(e)(1) that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

## Statement under 37 C.F.R. §1.704(d)

Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the information disclosure statement.

No fee is believed to be required for this Information Disclosure Statement. If a fee is required, please charge the fee to deposit account 50-2257. This paper is submitted in duplicate.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 25, 2003.

Attorney for Applicant(s)

Date of Signature

Respectfully submitted,

Michael Shenker

Attorney for Applicant(s)

Michael Sharker

Reg. No. 34,250